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CONFIRMATION NO. 5443

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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a DIV of 10/400,469 03/28/2003 \*  
 (\*)Data provided by applicant is not consistent with PTO records. *bn*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2002-115924 04/18/2002 *pn*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 56	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature <i>PN</i>	Initials	

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TITLE  
 Semiconductor integrated circuit device and a method of manufacturing the same

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